

XXXXXX = Specific Device Code A = Assembly Location

GENERIC

MARKING DIAGRAM*

XXXXXX

AYWZZ

2X 0.91

0.97

4X 1.00-

4X 0.75-

RECOMMENDED

MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the IN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

Y = Year W = Work We

W = Work Week
ZZ = Lot Traceability

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot " ", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	DFN5 5x6, 1.27P (SO-8FL)		PAGE 1 OF 1

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BOTTOM VIEW

(EXPOSED PAD)

PITCH